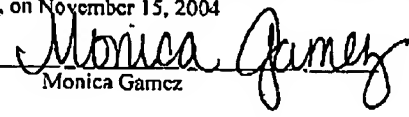


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICEIn re application: **Johnson**Serial No.: **10/001,518**Filed: **October 30, 2001**For: **Power Monitoring and Reduction
for Embedded IO Processors**§
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§Group Art Unit: **2182**Examiner: **Fritz M. Fleming**Attorney Docket No.: **01-645****Certificate of Transmission Under 37 C.F.R. § 1.8(a)**I hereby certify this correspondence is being transmitted via
facsimile to the Commissioner for Patents, P.O. Box 1450,
Alexandria, VA 22313-1450, facsimile number (703) 872-
9306, on November 15, 2004

By:


Monica Gamcz**RESPONSE TO NOTICE OF NON-COMPLIANCE**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

No fees are believed to be required. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to LSI Logic Corporation Deposit Account No. 12-2252. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to LSI Logic Corporation Deposit Account No. 12-2252.

In response to the Notice of Non-Compliance dated October 15, 2004, please amend the above-identified application as follows:

Listing of claims begins on page 2 of this paper.

Remarks begin on page 6 of this paper.

Page 1 of 6
Johnson - 10/001,518**BEST AVAILABLE COPY**

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